## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

Corisis et. al.

Serial No.: Not Yet Assigned

**Filed:** August 22, 2003

For: METHODS FOR DESIGNING BOND PAD REROUTING ELEMENTS FOR USE

IN STACKED SEMICONDUCTOR DEVICE ASSEMBLIES AND FOR ASSEMBLING SEMICONDUCTOR

**DEVICES** 

Confirmation No.: Unknown

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-4814.1US

(01-0040.01/US)

NOTICE OF EXPRESS MAILING

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Date of Deposit with USPS: August 22, 2003

Person making Deposit: Chris Haughton

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The present application is a divisional of application Serial No.10/118,366, filed April 8, 2002, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior application is enclosed herewith.

## Attorney Docket No. 2269-4814.1US

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,

Brick G. Power Registration No. 38,581 Attorney for Applicants

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Date: August 22, 2003

BGP/dlm:djp

Enclosures: Form PTO-1449 or PTO/SB/08

Document in ProLaw

Sheet 1 of 1

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION				Docket Number (Optional) 4814.1US (01-0040.01/US)		Application Number Not Y t Assigned		
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